

Form PTO-1449 (modified 2/91)	U.S. DEPT OF COMMERCE Patent and Trademark Office	Attorney Docket Number: MERL-1559	Serial Number: 10/802,468
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant: Perry et al.	
		Filing date: March 16, 2004	Group art area: 2676

U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub-class	Filing date if appropriate
DW	US2003/0067468 A1	10 Apr 2003	Duluk, JR et al.	345	506	✓
DW	US2004/0189653 A1	30 Sept 2004	Perry et al.	345	557	✓

FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Subclass	Translation	
						YES	NO
DW	WO03/081445 A1	2 Oct 2003	WIPO	G06F	15/00		X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

1.	Taylor, M. B. et al. "Scalar Operand Networks: On-Chip Interconnect for ILP in Partitioned Architectures" High-Performance Computer Architecture, 2003. 8-12 Feb 2003, pp. 341-353. XP-010629526.
2.	Scott, N. D. et al. "An Overview of the VISUALIZE fx Graphics Accelerator Hardware" Hewlett-Packard Journal, vol. 49, no. 2, pp. 28-34, May 1998. XP-000865343.
3.	
4.	
5.	

Examiner: <i>Dan WK</i>	Date Considered: 3/17/06
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP .609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	